

•Field Asynchronous Circuit & •Office

system Design

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| Educational background

■ 1996 ~ 2005 Gwangju Institute of Science and Technology (Doctor of Engineering – VLSI /CAD)

■ 1996 ~ 1998 Gwangju Institute of Science and Technology (Master of Engineering – VLSI /CAD)

■ 1992 ~ 1996 Hallym University (Bachelor of Science – The Department of Computer Science)

Major Careers

■ 2010 Project Manager of Nurturing EXcellent engineers in information Technology Project (NEXT Project)

College of Engineering A1201

- 2009 ~ 2010 Member of Business Operation Committee of HEGA - NURI
- 2008 ~ 2009 Member of Steering Committee, Nurturing EXcellent engineers in information Technology Project (NEXT Project)
- 2007 ~ 2008 Research Professor of Gwangju Institute of Science and Technology
- 2006 Short-term Visiting Researcher of UCLA
- 2005 ~ 2007 University of Cambridge

Postdoctoral Researcher